AMENDMENTS TO THE ABSTRACT

METHOD FOR MIN-CUT AND RATIO MIN-CUT PARTITIONING

Abstract of the Disclosure

The method of Edge-Node Interleave Sort for Leaching and Envelop (ENISLE) eomprises-method of mapping a circuit into a VIE-V-E plainplane to transform a circuit information into a V-E plainplane. A plurality of sSorting is performed for obtaining mincut or/and ratio min-cut partitioning. The sorting includes (1) performing a first-sorting step from an edge view based on a bottom side of the V-E plainplane; (2) performing a second sorting step from an a node view based on a right side of the V-E plainplane; (3) performing a third sorting from said edge view based on a top side of the V-E plainplane; and (4) performing a fourth sorting step from said node view based on a left side of the V-E plainplane.

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